



Berenbaum 7-2-3-3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Berenbaum et al.  
Case: 7-2-3-3  
Serial No.: 09/538,670  
Filing Date: March 30, 2000  
Group: 2154  
Examiner: Larry Donaghue

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: John M. Mason Date: August 29, 2003

Title: Method and Apparatus for Allocating Functional Units in a Multithreaded VLIW Processor

TRANSMITTAL OF FORMAL DRAWINGS

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Technology Center 2100

Commissioner for Patents  
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Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith five (5) sheets of formal drawings in the above-referenced application.

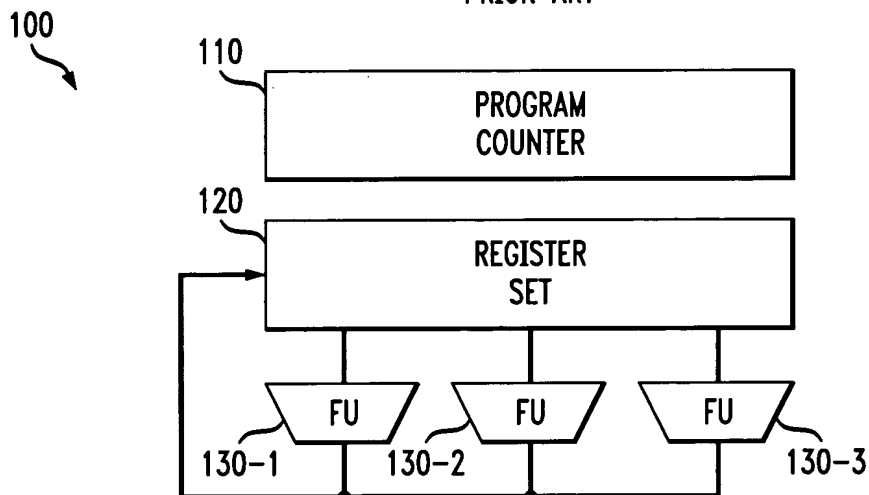
Respectfully submitted,

Kevin M. Mason

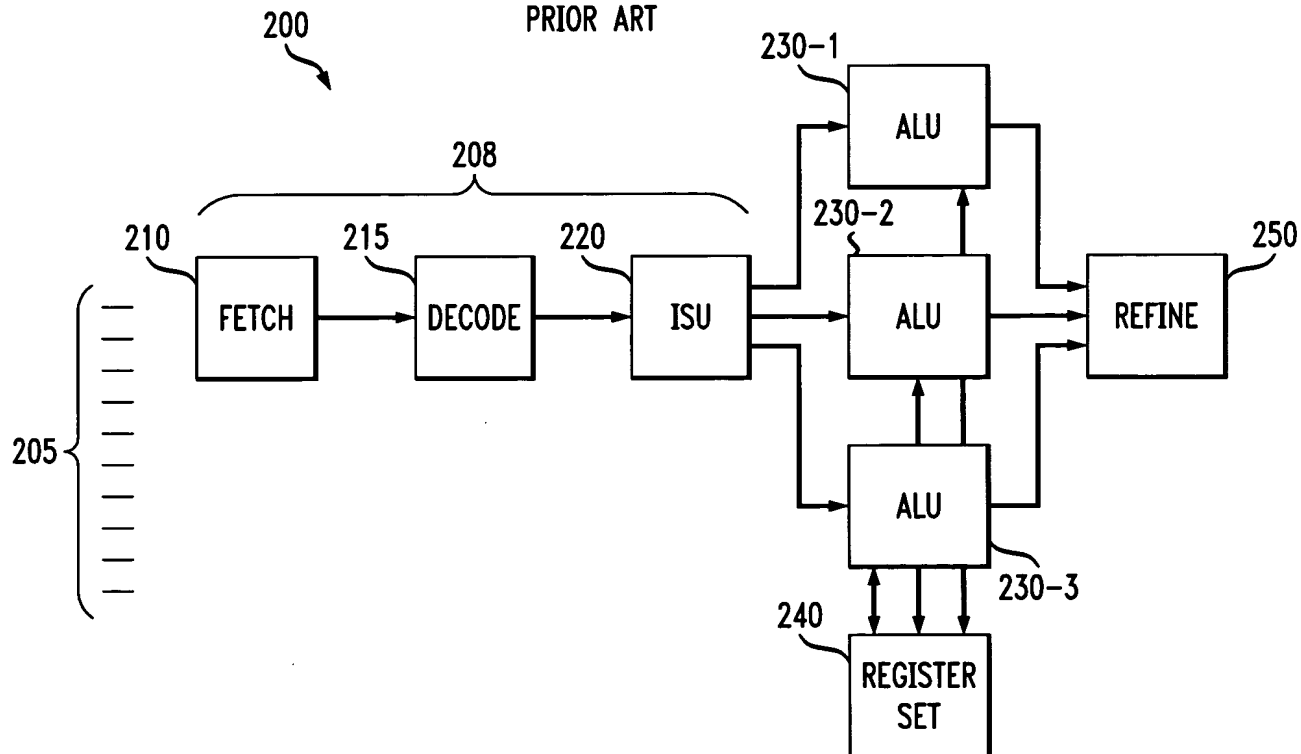
Date: August 29, 2003

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*FIG. 1*  
PRIOR ART



*FIG. 2*  
PRIOR ART

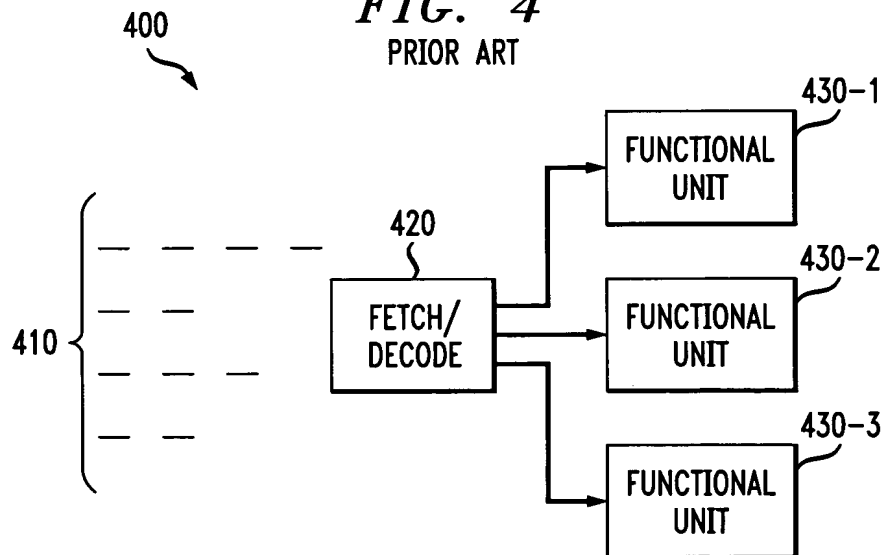


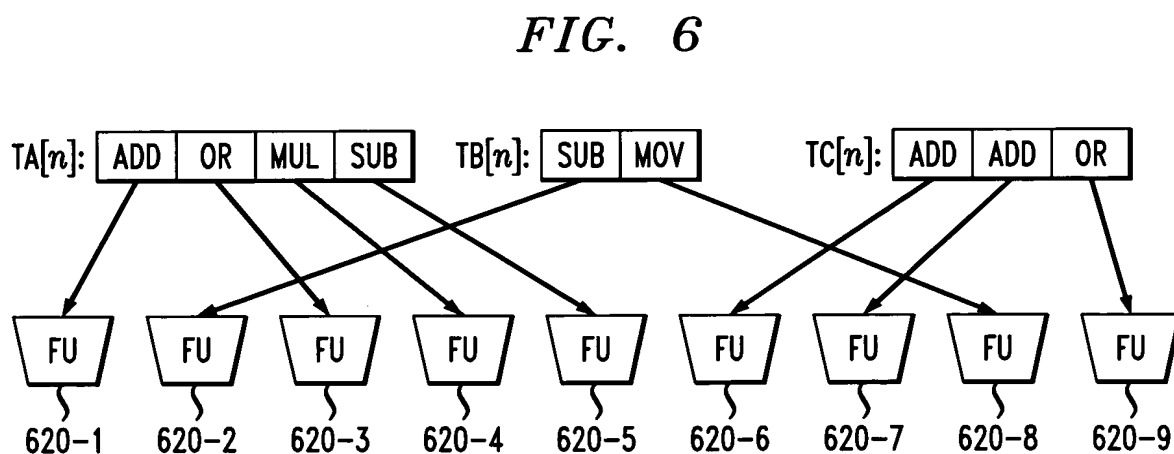
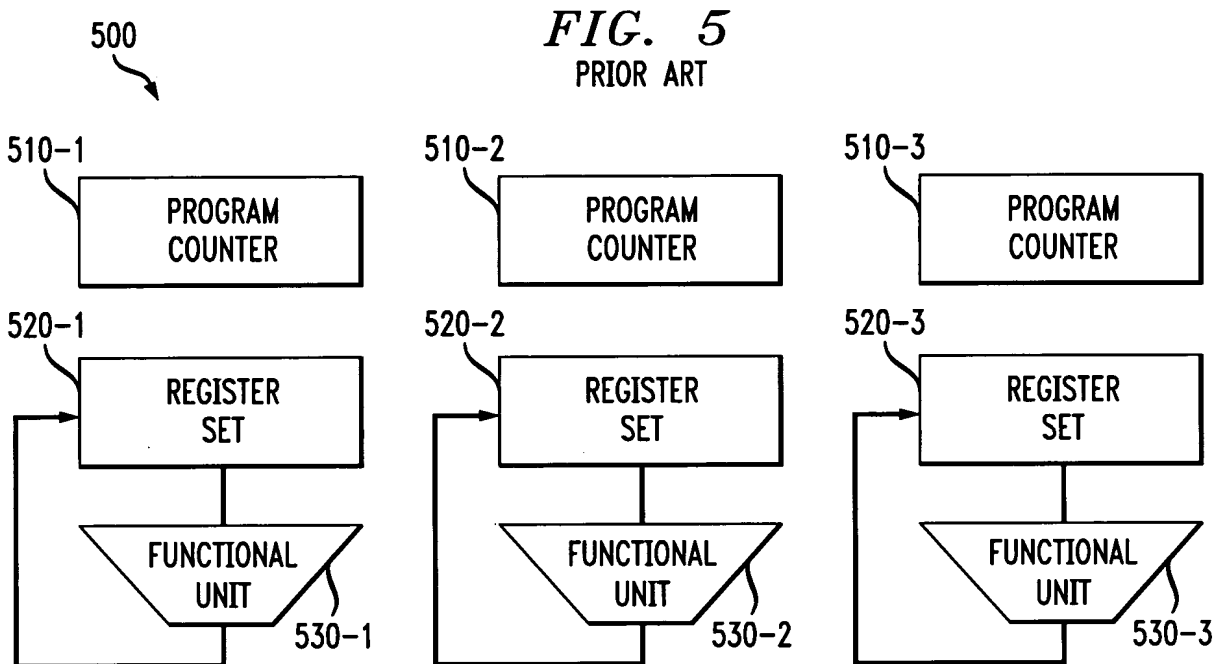


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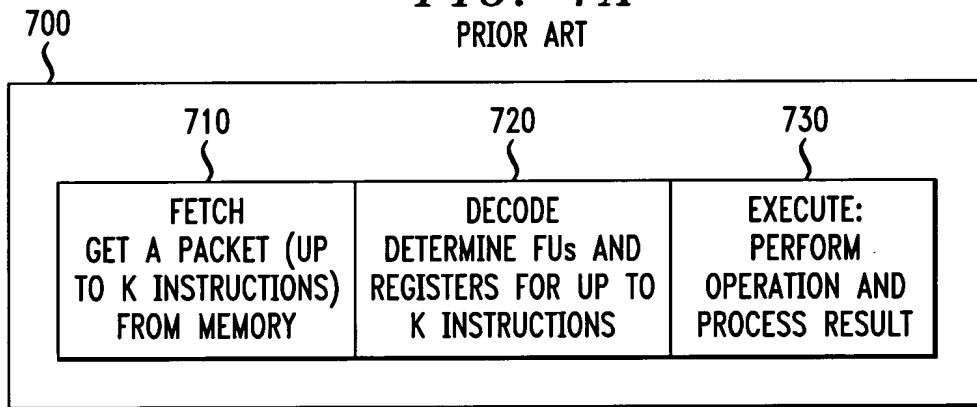
*FIG. 3*

310 { L3: ADD R0, R1, R2  
L2: SUB R3, R4, R2  
L1: OR R6, R1, R5

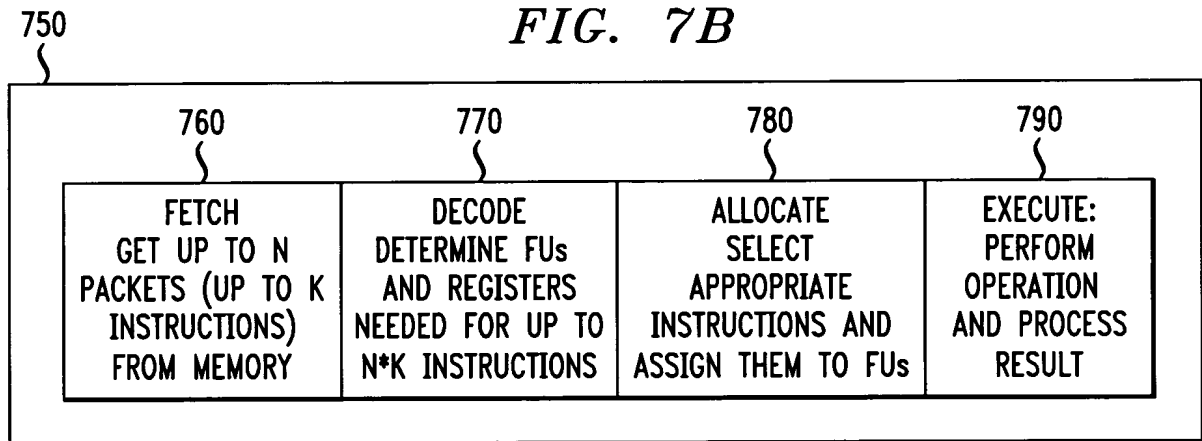
*FIG. 4*  
PRIOR ART



**FIG. 7A**  
PRIOR ART



**FIG. 7B**





780

FIG. 8

